

DOI: 10.17725/rensit.2023.15.109

Dynamic thermoelectric model of a heterojunction bipolar transistor taking into account the voltage drop on the emitter metallization tracks

^{1,2}Vyacheslav A. Sergeev, ¹Alexander M. Hodakov

¹Kotelnikov Institute of Radioengineering and Electronics of RAS, Ulyanovsk Branch, <http://www.ulireran.ru/>

Ulyanovsk 432071, Russian Federation

²Ulyanovsk State Technical University, <https://www.ulstu.ru/>

Ulyanovsk, 432027, Russian Federation

E-mail: sva@ulstu.ru, hod22am@mail.ru

Received March 20, 2023, peer-reviewed March 27, 2023, accepted April 06, 2023

Abstract: A dynamic 3D thermoelectric model has been developed to calculate the temperature field and emitter current density in the comb structure of a heterojunction bipolar transistor (HBT) with the length of the emitter metallization paths comparable to the size of the crystal, taking into account the inhomogeneous distribution of current density under the emitter paths caused by a voltage drop on the resistance of the current-carrying metallization. The model is based on an iterative solution in the COMSOL Multiphysics software environment of a non-stationary heat equation together with a system of equations for the distribution of electric potential along the emitter path and the current density under the path. It is shown that during the action of the heating power pulse in the HBT, the distribution of temperature and current density along the emitter tracks change character, respectively, from homogeneous and monotonically decreasing to non-monotonically changing. At the same time, the maximum temperature and current density reach stationary values with a rate significantly exceeding the rate of overheating increase with homogeneous heating of the structure, and the maxima of temperature and current density in the process of self-heating shift from the beginning to the center of the tracks. The proposed model can be used to evaluate the thermomechanical stresses in the structure of the HBT and the limiting electrical parameters in the pulsed modes of operation of the HBT.

Keywords: heterojunction bipolar transistor, dynamic thermoelectric model, current density, temperature, inhomogeneity

UDC 621.382.029

Acknowledgments: This work was supported by the Russian Science Foundation (project no. 22-29-01134).

For citation: Vyacheslav A. Sergeev, Alexander M. Hodakov. Dynamic thermoelectric model of a heterojunction bipolar transistor taking into account the voltage drop on the emitter metallization tracks. *RENSIT: Radioelectronics. Nanosystems. Information Technologies*, 2023, 15(2):109-116e. DOI: 10.17725/rensit.2023.15.109.

CONTENTS

1. INTRODUCTION (110)
 2. THERMOELECTRIC MODEL (111)
 3. PROBLEM SOLUTION AND CALCULATION RESULTS (112)
 4. CONCLUSION (114)
- REFERENCES (115)

1. INTRODUCTION

Along with the active development of MIS and HEMT microwave transistors, modern radio and telecommunications equipment widely uses high-power bipolar (BT), including heterojunction (HBT), microwave transistors [1-4]. Devices of this class are the least reliable as part of modern radio-electronic systems for various purposes, since they operate in the most severe thermal and electrical conditions. This class of devices is characterized by the presence of a strong positive thermal feedback (PTF) and the manifestation of the effects of an inhomogeneous and unstable distribution of current density, power and temperature in transistor structures [5-7], which lead to local overheating and thermomechanical stresses of the structure and, as a result, to the acceleration of degradation mechanisms and instrument failures.

One of the most common geometries of the structures of modern high-power BTs and HBTs is a strip or comb geometry with a parallel arrangement of elementary transistors (cells) of HBTs (see **Fig. 1** [4] and **Fig. 2** [7]). Self-heating of each HBT cell by dissipated power and thermal coupling between neighboring cells lead to an uneven temperature profile of the HBT elementary transistor matrix. Due to the positive temperature coefficient of the emitter current, currents of higher density will flow through the central cells of the HBT with a higher temperature, which

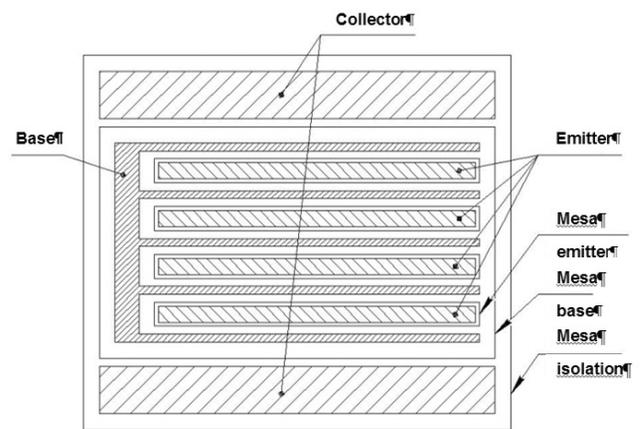


Fig. 1. Topology of the HBT active region [4].

leads to an increase in heat release, which can ultimately lead to thermal breakdown or degradation of the device [8, 9], which is especially strong. manifests itself at high levels of injection [10].

In order to reduce the uneven temperature distribution and solve these thermal problems, various options for one-dimensional geometry design are used, including changing the length of the emitter in HBT cells [7] and changing the distance between emitters between HBT cells [8], which allows to reduce the temperature difference between individual cells.

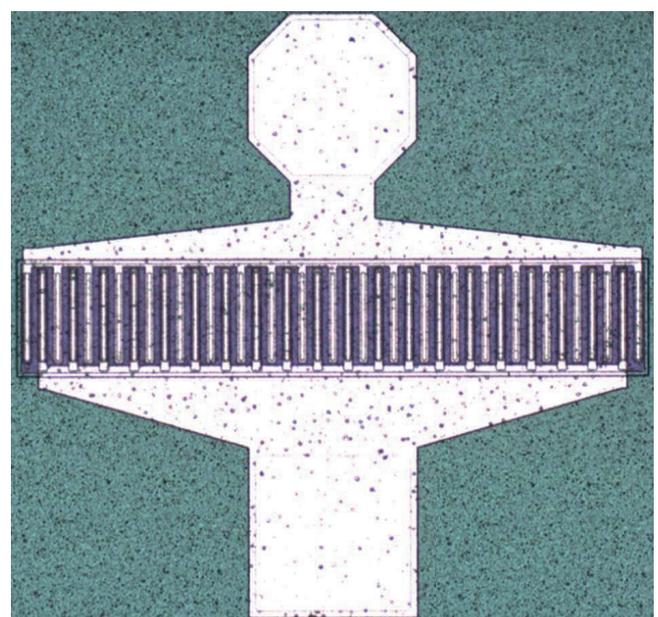


Fig. 2. Comb structure of HBT with 20 elementary transistors [7].

In well-known works, thermoelectric processes in comb-like HBT structures were considered without taking into account the voltage drop on the current-carrying tracks of emitter metallization, which leads to a significant inhomogeneous distribution of the emitter current density, and hence the power dissipation density along the emitter tracks [11-13].

In [14], we considered a stationary thermoelectric model of the HBT microwave transistor structure, taking into account the voltage drop on the current-carrying tracks of the emitter metallization. It is shown that the distributions of temperature and current density along the emitter tracks of the structure change their character: from monotonically and weakly decreasing from the beginning of the track to the end in the isothermal approximation, these distributions become nonmonotonic and essentially inhomogeneous. In addition, the maximum current density and temperature shift from the beginning to the center of the track with an increase in the operating current. Note that for semiconductor structures, it is not the absolute deviation of the local temperature of the structure from the average value that is critically important, but the gradient of temperature change, which determines the level of thermomechanical stresses and the dynamics of electromigration processes in the elements of the structure [15].

In radio-electronic systems of HBT microwave transistors, as a rule, operate not in stationary, but in pulsed modes with the duration and duty cycle of the pulses varying over a wide range. In this case, it is obvious that during the action of the dissipated power pulse, the distribution of current density and temperature in the structure will be transformed. This article

presents a 3D thermoelectric model for calculating the temperature field and the distribution of the emitter current density in the comb structure of the GBT, taking into account the joint influence of all the above factors of the inhomogeneous distribution of current and temperature in the device structure, including the inhomogeneous distribution of the current density under the emitter tracks as a result of the voltage drop across resistance of current-carrying metallization.

2. THERMOELECTRIC MODEL

Fig. 3 shows the geometry of the HBT semiconductor structure model used in calculating the temperature fields, which is a rectangular semiconductor crystal with dimensions $l_x \times l_y \times l_z$, with emitter metallization tracks located on its upper surface with dimensions $a_e \times L_e \times b_e$. The crystal is placed on an ideal heat sink with temperature T_0 . As in [14], assuming the emitter tracks to be narrow, we neglect the effect of pushing the emitter current to the side edges of the tracks along the coordinate x .

The temperature field in the HBT structure $T(x,y,z,t)$ at an arbitrary time t in the process of self-heating of the structure by a heating power pulse is found from the solution of the non-stationary heat conduction equation

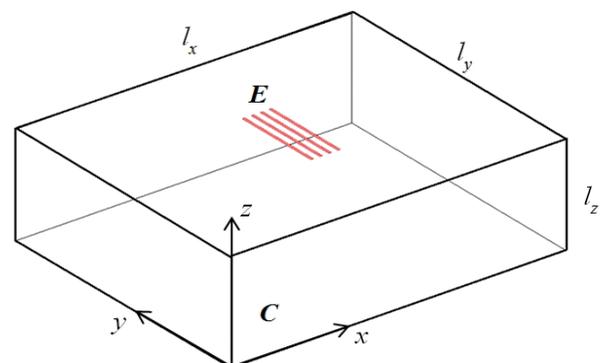


Fig. 3. Geometry of the HBT structure model: E – emitter, C - semiconductor crystal.

$$c(T) \rho(T) \frac{\partial T}{\partial t} + \nabla_{x,y,z} (\lambda(T) \nabla_{x,y,z} T) = 0, \quad (1)$$

where c , ρ , λ – coefficients of heat capacity, density and thermal conductivity of the crystal material, with the following boundary conditions:

- side surfaces and the upper surface of the crystal are thermally insulated;
- the temperature of the lower surface of the crystal is equal to the temperature of the heat sink T_0 ;
- on the upper surface in the region of the emitter of the structure, the heating power density is given, which is determined by the formula:

$$q_e(T) = -\lambda(T) \left. \frac{\partial T}{\partial z} \right|_{z=l_z} = \begin{cases} J_e(T) U_c, & (x, y) \in S_e \\ 0, & (x, y) \in S - S_e \end{cases}, \quad (2)$$

where S , $S_e = n a_e L_e$ – area of the upper surface of the crystal and its active region, n – number of emitter tracks, J_e , U_c – emitter current density and collector voltage.

As an initial condition for the temperature in solving equation (1), a uniform temperature distribution over the HBT structure was taken $T(x, y, z, 0) = T_0$.

Emitter current density distribution J_e under the track of the emitter metallization of the structure is found from the solution of the following system of equations:

$$J_e(T) = J_{e0} (T/T_0)^3 \exp \left\{ \frac{-E_g + e(U_c - \varphi_e - r S_e n^{-1} J_e(T))}{kT} \right\}, \quad (4)$$

where J_{e0} – parameter weakly dependent on temperature, U_c – direct voltage drop across the emitter p - n junction, E_g – semiconductor bandgap, e – electron charge, φ_e – emitter metallization potential, r – transistor input ohmic resistance, k – Boltzmann's constant;

$$\frac{dJ_{em}(y)}{dy} = -\frac{J_e(y)}{h_e}, \quad (5)$$

$$\frac{d\varphi_e(y)}{dy} = -\frac{J_{em}(y)}{\sigma_{em}}, \quad (6)$$

$$J_{em}(y_{eb}) = I_e / h_e a_e, \quad (7)$$

$$J_{em}(y_{ee}) = 0, \quad (8)$$

$$\left. \frac{d\varphi_e}{dy} \right|_{y=y_{eb}} = -\frac{I_e}{\sigma_{em} h_e a_e}, \quad (9)$$

$$\left. \frac{d\varphi_e}{dy} \right|_{y=y_{ee}} = 0, \quad (10)$$

where I_e – total emitter current, J_{em} – current density by emitter track metallization, y_{eb} and y_{ee} – track start and end coordinates, σ_{em} – specific conductivity of emitter track metallization.

To ensure the condition for switching on the transistor in an electrical circuit (constancy of the total emitter current I_e) it is necessary that the equality be satisfied:

$$\iint_{S_e} J_e(x, y, t) dx dy = I_e. \quad (11)$$

3. SOLUTION OF THE PROBLEM AND RESULTS OF CALCULATIONS

The model non-stationary problem (1)-(11) was solved by the method of successive time intervals [16], that is, the entire time of the heat transfer process was divided into a number of intervals $\Delta t = t_j - t_{j-1}$, within which the non-stationary heat conduction equation was solved with the initial power density $q_{ej}(x_j, y_j, l_z, t_{j-1})$ and temperature $T_j(x_j, y_j, l_z, t_{j-1})$ distributions, found as a result of solving on previous interval. The values of the potentials U_c and φ_e were found by a numerical iterative method, a similar algorithm of which is presented in [17]. As an initial approximation of the dependence $J_e^0(y)$ in the iterative process, the current density values calculated by the formula [18] were chosen:

$$J_e^0(y) = \frac{2 \varphi_{r0}}{L_e a_e R_e} \cdot \frac{C^2}{\cos^2 [C(1 - y/L_e)]}, \quad (12)$$

where $\varphi_{T_0} = \frac{kT_0}{e}$ – temperature potential at $T_0 = 300$ K, the value of which is equal to 26 mB; R_c – metallization resistance of the emitter track, and the constant C is found from the solution of the equation $CtgC = R_c I_c / 2n\varphi_{T_0}$.

The original program included an appeal to the COMSOL Multiphysics interactive software environment. As in [14], an InGaP/GaAs GBT with a crystal size of $300 \times 250 \times 100$ microns and an active transistor structure with four gold emitter tracks with a width $a_e = 2$ microns and a thickness of $h_e = 0.5$ microns was chosen as the calculated base object of the study, while the length of the tracks during calculations varied and was set within $L_e = (40 \div 80)$ microns. The functional dependences on the temperature of the thermophysical characteristics c, ρ, λ of the crystal material were selected from the COMSOL program materials database. The temperature of the ideal heat sink T_0 was assumed to be 300 K.

In order to compare the calculation results with the results of calculations based on the stationary model, numerical calculations based on the proposed dynamic model are given for a variant of the transistor operating mode at $I_c = 40$ mA, $U_c = 7$ B. The initial value of the offset voltage at the emitter junction $U_e = 1.2$ B. The total current of the transistor structure was considered evenly distributed between the emitter metallization tracks.

Figures 4 and 5 show the dynamics of changes in the maximum and average temperature of the structure, as well as the maximum emitter current density of the HBT structure during the action of a heating power pulse.

Approximating the change in the maximum and average overheating of the structure by a function of the form $\Delta T(1 - \exp(-t/\tau_T))$, where τ_T thermal

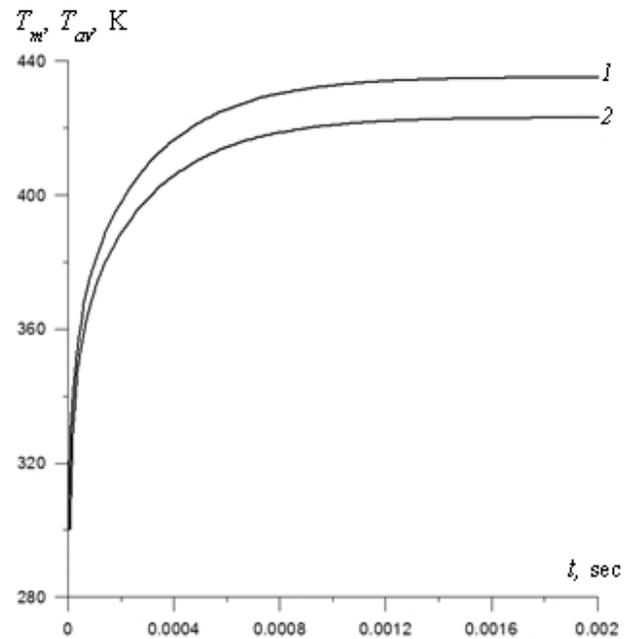


Fig. 4. Change in the maximum (1) and average (2) temperature of the active region: $I_c = 40$ mA, $U_c = 7$ V; $a_e = 2$ μ m, $L_e = 60$ μ m, $h_e = 0.5$ μ m.

time constant that determines the rate of increase of overheating, according to the obtained dependences, it is possible to estimate the change in the rate of increase of overheating as a result of inhomogeneous current distribution under the action of positive thermal feedback in

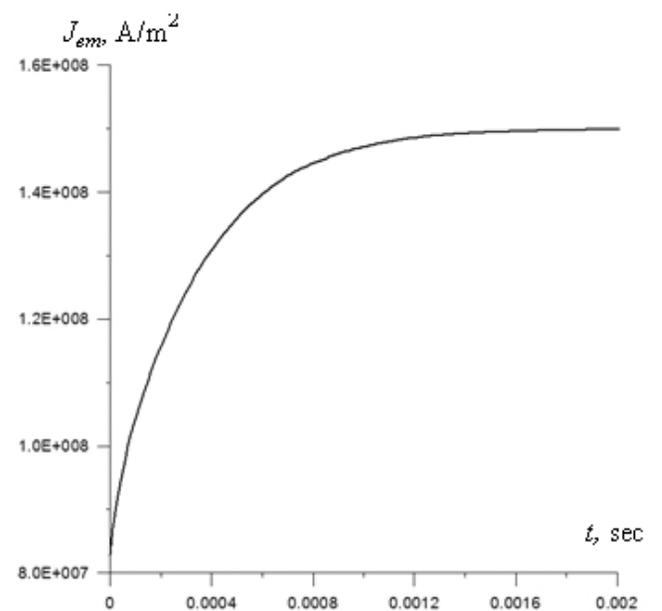


Fig. 5. Changing the maximum emitter current density: $I_c = 40$ mA, $U_c = 7$ V; $a_e = 2$ μ m, $L_e = 60$ μ m, $h_e = 0.5$ μ m.

the structure compared with the case of homogeneous heating of the structure. When the structure is heated by a uniformly distributed power density $\tau_T^{\text{homogen}} = \tau_{\text{Tcr}}$, where τ_{Tcr} thermal time constant of the crystal, determined by the thermal diffusivity of the crystal material and its thickness d : $\tau_{\text{Tcr}} = d^2/\alpha$. For a GaAs crystal with a thickness of 100 microns is about 300 microseconds. From the dependencies in Fig. 4 and Fig. 5, it can be seen and numerical calculations show, that the thermal time constant $\tau_T^{\text{heterogen}}$ with inhomogeneous heating and the action of PFT is approximately 130 microseconds, that is, 2.3 times less τ_T^{homogen} , that is, the rate of change in the maximum and average overheating of the structure significantly exceeds the rate of increase in overheating with homogeneous heating of the structure. This is obviously due to the redistribution of the current density along the emitter metallization tracks and, accordingly, the power density dissipated in the HBT collector junction under the track.

From the distributions of overheating along the emitter track presented in Fig. 6

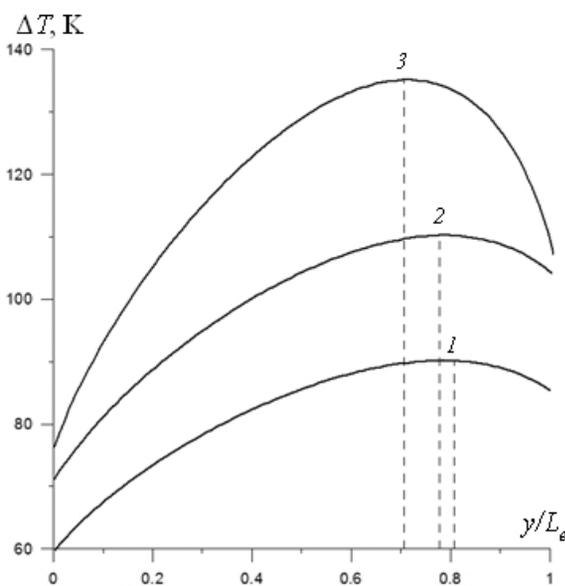


Fig. 6. Overheating along the 3rd emitter track: $I_e = 40 \text{ mA}$, $U_c = 7 \text{ V}$; $a_e = 2 \text{ }\mu\text{m}$, $L_e = 60 \text{ }\mu\text{m}$, $h_e = 0.5 \text{ }\mu\text{m}$; t : 1 – 0.15, 2 – 0.3, 3 – 2 ms.

at different times, it can be seen that in the process of HBT heating, the maximum temperature value shifts from the edge of the track to its center, i.e., to the center of the crystal.

It should also be noted again that the proposed model did not consider the redistribution of the total current of the structure between the cells as a result of uneven heating of the cells of the HBT structure due to their different arrangement with respect to the edges of the crystal. This effect will probably lead to even greater inhomogeneity of the temperature and current density distributions in the structure and requires a separate additional analysis.

4. CONCLUSION

The developed dynamic thermoelectric model of the HBT comb structure, taking into account the inhomogeneous distribution of the emitter current density as a result of the voltage drop on the emitter metallization tracks and the positive thermoelectric feedback acting in the HBT structure, determines the rate and nature of the change in the temperature and current density distribution along the emitter tracks of the structure under the action of a pulse heating power in HBT: from monotonically and weakly decreasing from the beginning of the track to the end in the isothermal approximation, these distributions become nonmonotonic and significantly inhomogeneous. In this case, the maximum temperature and current density reach stationary values at a rate significantly exceeding the rate of increase in overheating during uniform heating of the structure, and the temperature and current density maxima in the process of self-heating of the structure shift from the beginning to the center of

the tracks. The proposed model can be used in the development of HBT structures and evaluation of their limiting functionality in terms of current and temperature in the pulsed operating modes of the HBT.

REFERENCES

1. Jianjun Gao. *Heterojunction Bipolar Transistors for Circuit Design: Microwave Modeling and Parameter Extraction*. United States, John Wiley & Sons Inc, 2015, 280 p.
2. Xin Wen, Akshay Arabhavi, Wei Quan. Performance Prediction of InP/GaAsSb Double Heterojunction Bipolar Transistors for THz applications. *J. Appl. Phys.*, 2021, 130:034502.
3. Lachner R. Industrialization of mmWave SiGe technologies: Status, future requirements and challenges. *IEEE 13th Topical Meeting on Silicon Monolithic Integrated Circuits in RF Systems*, 2013:105-107.
4. Kozlovskij EYu, Zaharov SI, Semenova LM, Tejder AA. Razrabotka tekhnologii izgotovleniya geterobipolyarnykh tranzistorov na osnove struktur InGaP/GaAs [Development of manufacturing technology for heterobipolar transistors based on InGaP/GaAs structures]. *Sb. trudov 31-oy Mezhdunarodnoj konferencii "SVCH-tekhnika i telekommunikatsionnye tekhnologii, KryMiKo"*, Sevastopol, SevGU, 2021, 3:27-29 (in Russ.).
5. Lee CP, Chau FHF, Ma W, Wang NL. The Safe Operating Area of GaAs-Based Heterojunction Bipolar Transistor. *IEEE Trans. Electron.*, 2006, 53(11):2681-2688.
6. Chen Liang. Thermal stability improvement of a multiple finger power SiGe heterojunction bipolar transistor under different power dissipations using non-uniform finger spacing. *Chinese Physics B*, 2011, 20:018501.
7. Jin Dongyue. Thermal stability of the power SiGe HBT with non-uniform finger length. *Proc. International Conference on Microwave and Millimeter Wave Technology*, 2008:166-169, doi: 10.1109/ICMMT.2008.4540331.
8. Dongyue Jin, Wanrong Zhang, Hongyun Xie, Liang Chen, Pei Shen, Ning Hu. Structure optimization of multi-finger power SiGe HBTs for thermal stability improvement. *Microelectronics Reliability*, 2009, 49(4):382-386.
9. Rui Chen. Thermal resistance matrix representation of thermal effects and thermal design in microwave power HBTs with two-dimensional array layout. *Chinese Phys. B*, 2019; doi: 10.1088/1674-1056.ab3436.
10. Lu Z, Zhou L, X. Hu X. Electro-Thermal analysis of SiGe HBT under HPM Injection. *IEEE MTT-S International Conference on Numerical Electromagnetic and Multiphysics Modeling and Optimization (NEMO)*, 2020:1-4.
11. Caves KY, Barnes IA. Optimum length of emitter stripes in "comb" structure transistors. *IEEE Trans*, 1965, ED-12(2):84-85.
12. Sergeev VA, Khodakov AM. Two-Section Model of the Current Distribution in Strip Layouts of Bipolar and Hetero-Bipolar Microwave Transistors. *Journal of Communications Technology and Electronics*, 2022, 67(11):1400-1405.
13. Sergeev VA. Analiticheskaya model neizotermicheskogo raspredeleniya plotnosti moshchnosti v strukturakh bipolyarnykh tranzistorov [Analytical model of non-isothermal power density distribution in bipolar transistor structures]. *Izvestiya vuzov. Elektronika*, 2005, 3:22-28, (in Russ.).

14. Sergeev VA, Hodakov AM. Thermoelectric model of a heterojunction bipolar transistor taking into account the voltage drop on the current-carrying metallization. *RENSIT: Radioelectronics. Nanosystems. Information technologies*, 2022, 14(2):103-110. DOI: 10.17725/rensit.2022.14.103.
15. Hodakov AM, Tarasov RG, Sergeev VA, Kulikov AA. Thermal deformation model of the submodule of the X-band output power amplifier. *RENSIT: Radioelektronika. Nanosistemy. Informacionnye Tehnologii*, 2021, 13(1):13-18. DOI: 10.17725/rensit.2021.13.013.
16. Kozdoba LA. *Reshenie nelinejnyh zadach teploprovodnosti* [Solving nonlinear heat conduction problems]. Kiev, Naukova dumka Publ., 1976, 136 p.
17. Sergeev VA, Hodakov AM. *Nelinejnye teplovyje modeli poluprovodnikovyx priborov* [Nonlinear thermal models of semiconductor devices]. Ul'yanovsk, UIGTU Publ., 2012, 159 p.
18. Sergeev VA, Hodakov AM. Thermal model of a heterojunction bipolar transistor taking into account the voltage drop on the emitter fingers of metallization. *Moscow Workshop on Electronic and Networking Technologies (MWENT)*, 2022. DOI: 10.1109/MWENT55238.2022.9802430.